Europass Curriculum Vitae

Personal information

Photo

First name / Family name	**Paolo PRINETTO**
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Nationality	Italian
Date of birth	17.03.1953
C.F.	PRN PRN 53C17 D933 T
Gender	Male
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Work experiences

01.11.1990 - today	Full professor of Computer Engineering at the Dipartimento di Automatica e Informatica (CE Dept) of Politecnico di Torino, Turin (Italy) (Settore Concorsuale 09/H1 - Sistemi di Elaborazione delle Informazioni)
2004-today	Adjoint Professor of the University of Illinois at Chicago, IL (USA).
14.10.1987 - 31.10.1990	Full professor of Computer Science at the Dipartimento di Storia (History Dept) of the Università di Udine, Udine (Italy) (gruppo concorsuale 311: prima disciplina "Automi e Linguaggi Formali" - Corso di Laurea in "Conservazione dei beni culturali" della Facoltà di Lettere e Filosofia)
1.03.1982 - 13.10.1987	Researcher (Ricercatore confermato) at the Dipartimento di Automatica e Informatica (CE Dept) of Politecnico di Torino, Turin (Italy)
1.03.1978 - 28.02.1982	Research Assistant (Assistente incaricato supplente) at the Istituto di Elettrotecnica Generale (EE Institute) of Politecnico di Torino, Turin (Italy)
1.11.1976 - 28.02.1978	Teaching Assistant (Coadiutore alle esercitazioni) at the Istituto di Elettrotecnica Generale (EE Institute) of Politecnico di Torino, Turin (Italy)

Education and training

25.10.1976	Laurea (M.S. equivalent) in Electronic Engineering from Politecnico di Torino, Turin (Italy) (110/110 cum laude)
1971	Maturità Scientifica – Liceo Scientifico G. Segré, Turin (Italy) (60/60 cum laude)

Languages

Mother tongue	Piemonteis
Other languages	Italian, English, French, Russian

Self-assessment
European level (*)

Language	Understanding		Speaking		Writing
	Listening	Reading	Spoken interaction	Spoken production	
English	C2	C2	C2	C2	C2
French	C2	C2	C2	C2	C2
Russian	A1	A1	A1	A1	A1

(*) Common European Framework of Reference for Languages

Teaching Activities

Current

- Instructor of the courses:
  - *01NRJOV - Reliable embedded systems design*
  - *01MNNNX - Algoritmi e calcolatori (Algorithms and Computer Architectures)*
  - *01OQM - Tecnologie per la disabilità (Assistive Technologies)*

  for CE and EE tracks at Politecnico di Torino, Turin (Italy)
- Instructor of the courses:
  - *ECE465 - Digital Systems Design*
  - *ECE564 - Advanced Digital Systems Design*

  within the Master of Engineering (MEng) Degree Program of the University of Illinois at Chicago, IL (USA)
- Instructor of the courses:
  - *Computer Architectures*
  - *ECE564 - Advanced Digital Systems Design*

  within the Engineering Faculty of the Università Telemetica UniNettuno, Rome (Italy)

Past

2006-2014 Instructor of the courses:
- *Computer Architectures*
- *Digital System Design*

within the Engineering Faculty of the Università Telemetica UniNettuno, Rome (Italy)

2008-2011 Instructor of the course:
- *Embedded Systems Development*

at the European Master on Critical Networked Systems organized by the Università degli studi di Napoli Parthenope, Naples, Italy

2010 Instructor of the courses:
- *Digital System Testing*
- *Trends in HDL-based Automated Synthesis*

at the Master Universitario di II livello on "Sustainability, Safety and Security in Transportation Systems and Infrastructures" organized by the Università degli Studi di Genova, Genoa, Italy

2008 Instructor of the course:
- *Advanced Computer Architectures*

within the Engineering Faculty of the Università degli Studi di Napoli "Federico II", Naples, Italy

Research Activities	Research activities mainly focused on:

Research activities mainly focused on:
- *Digital Systems Design & Test*
- *Emerging Memories*
- *Hardware security & trust*
- *Design-for-Testability, Built-in Self Test & Built-in Self Repair methodologies*
- *System Dependability*
- *FPGA-based designs*
- *ICT and Assistive Technologies for people with disabilities.*

Research Project Leadership

Responsible of several research contracts with domestic and international Industries and Research Institutions, including: UE, ASI (Agenzia Spaziale Italiana), CNR (Italian National Research Council), Ministero dell'Istruzione, dell'Università e della Ricerca, Istituto Superiore Mario Boella, EDF Electricité De France (France), ASSET (USA), Siemens ICN, Italtel SpA, Ansaldo Trasporti, Ansaldo Signalling, ThalesAlenia Space, Magneti Marelli, Bottero, Selca, Aurelia Microelettronica, Yogitech, …

In particular, Project Leader of the following research projects:

2012-2014
LIS4ALL*: Lingua Italiana dei Segni per l'Accesso su Larga scala a informazioni Localizzate*, (Total Eligible Costs > 785.000 €) (Programma Operativo Regionale "Competitività regionale e occupazione" F.E.S.R. 2007-2013 - Asse 1: Innovazione e transizione produttiva - Attività I.1.3: Innovazione e PMI), which involved: Politecnico di Torino, Università di Torino (Dip di Informatica), BEPS Engineering, CSP – Innovazione nelle ICT, Regola, People&Communication.
The project targeted the development and the large-scale implementation of a new communication service in LIS (Italian Sign Language), devoted to the broadcasting of information and messages to the deaf on mobile devices

2009-2012
ATLAS*: Automatic Translation into Sign Languages* (Total Eligible Costs > 4.300.000 €), ("Converging Technologies" CIPE 2007 - Regione Piemonte - Settore di ricerca: Cognitive Science and ICT), which involves: Politecnico di Torino, Università di Torino (Dip di Informatica, & Dip di Psicologia), RAI Radiotelevisione Italiana S.p.A., BEPS Engineering, Lumiq Studios S.r.l., CSP – Innovazione nelle ICT, Microsoft Innovation Center, Virtual Reality and Multimedia Park, Cooperativa scs Global Communication, Fondazione Bruno Kessler – Trento, University of Illinois at Chicago.
The project exploited the convergence between cognitive sciences and ICTs to build innovative services to provide deaf people with the access to broadcast communications, through the automatic translation of the written Italian natural language into the animated Italian Sign Language (LIS)

2006-2007
COMMUTA*: Mutant hardware/software components for dynamically reconfigurable distributed systems,* (PRIN 2005: Programmi di Ricerca Scientifica di Rilevante Interesse Nazionale) (Total Eligible Costs > 225.000 €), which involved: Politecnico di Torino, Università degli Studi di Milano Bicocca (Milan, Italy), Università degli Studi di Napoli "Federico II" (Naples, Italy)

2001-2002
TEST-DOC*: Quality and Reliability of Complex Systems-on-Chip* (Total Eligible Costs > 1.200.000 €), which involved: Politecnico di Torino, Universitat Politècnica de Catalunya (Barcelona, Spain), LIRMM (Laboratoire d'Informatique Robotique et Microélectronique de Montpellier) (Montpellier, France), Siemens Information & Communication Networks (Milan, Italy), LogicVision, Inc. (San Jose CA, USA), Virage Logic, Inc (Fremont (CA, USA))

Editorial Activities

Journal Board Membership	Member of the Editorial Boards of the following international journals and magazines:
2010-today	ВЕСТНИК Томского государственного университета: УПРАВЛЕНИЕ, ВЫЧИСЛИТЕЛЬНАЯ ТЕХНИКА И ИНФОРМАТИКА (edited by the Tomsk University, Russia)
2009-today	International Journal of Critical Computer-Based Systems (IJCCBS) (edited by Interscience Publishers)
1998-2008	JETTA: Journal of Electronic Testing: Theory and Applications (edited by Kluwer, Academic Publishers, Boston, MA (USA))
2004-2005	IEEE Design & Test of Computers (edited by IEEE)
1989-1991	International Journal of Computer VLSI Design (edited by Ablex Publishing Corporation, Norwood, NJ (USA)) - Associate Editor
1987-1889	Alta Frequenza - Rivista di Elettronica (edited by AEI, Milano)
Conference Board Membership	Member of the Boards, in different roles, for the following international conferences:
Steering Committee Chair	Chair of the Steering Committees of the following international conferences:
2014-2016	IEEE ETS : European Test Symposium
Steering Committee Member	Member of the Steering Committees of the following international conferences:
2008-today	IEEE VTS : VLSI Test Symposium
2003-today	IEEE ETS : European Test Symposium
1997-2003	IEEE ETW : European Test Workshop
2000-2003	IEEE ITC : International Test Conference
General Chair	General Chair of the following international conferences and workshops:
2015	DTIS 2015: 10th International Conference on Design & Technology of Integrated Systems in Nanoscale Era, Naples (Italy) (April 2015)
2014	IEEE AQTR 2014: International Conference on Automation, Quality and Testing, Robotics, Cluj-Napoca (Romania) (May 2014) (Vice General Chair)
2014	EWDTS 2014: East West Design and Test Symposium, Kiev (Ukraine) (September 2014) (Vice General Chair)
2013	EWDTS 2013: East West Design and Test Symposium, Rostov-on-Don (Russia) (September 2013) (Vice General Chair)
2012	EWDTS 2012: East West Design and Test Symposium, Kharkov (Ukraine) (September 2012) (Vice General Chair)
2011	EWDTS 2011: East West Design and Test Symposium, Sevastopol (Ukraine) (September 2011) (Vice General Chair)
2010	EWDTS 2010: East West Design and Test Symposium, St. Petersburg (Russia) (September 2010) (Vice General Chair)
2007	IEEE VTS 2007: VLSI Test Symposium, Berkeley, CA (USA) (May 2007)

2005	IEEE ETS 2005: European Test Symposium, Tallinn (Estonia) (May 2005)
1997	IEEE ETW 1997: European Test Workshop, Chia Laguna, Cagliari (Italy) (May 1997)
1991	IFIP Advanced Research Workshop on Correct Hardware Design Methodologies, Turin (Italy) (April 1991)
1987	IEEE 5th European Workshop on Design For Testability, Castiglion della Pescaia, Grosseto (Italy) (June 1987)

Program Chair	Program Chair of the following international conferences and workshops:
2015	IEEE ETS 2015: European Test Symposium, Cluj-Napoca (Romania) (May 2015)
2014	DTIS 2014: 9th International Conference on Design & Technology of Integrated Systems in Nanoscale Era, Santorini (Greece) (May 2014)
2013	IEEE IDT 2013: 8th IEEE International Design and Test Symposium, Marrakesh, (Morocco) (December 2013)
2012	IEEE AQTR 2012: International Conference on Automation, Quality and Testing, Robotics, Cluj-Napoca (Romania) (May 2012)
2010	IEEE AQTR 2010: International Conference on Automation, Quality and Testing, Robotics, Cluj-Napoca (Romania) (May 2010)
2006	IEEE VTS 2006: VLSI Test Symposium, Berkeley, CA (USA) (May 2006)
2004	IEEE ETS 2004: European Test Symposium, Ajaccio (France) (May 2004)
2004	IEEE VTS 2004: VLSI Test Symposium, Napa, CA (USA) (April 2004) (vice program chair)
2003	IEEE VTS 2003: VLSI Test Symposium, Napa, CA (USA) (April 2003) (vice program chair)
2000	IEEE ETW 2000: European Test Workshop, Cascais (Portugal) (May 2000)

Program Committee Member	Member of the Program Committees of several international conferences and workshops; the complete list is in Appendix A.

Professional involvements

International

2010-2014	Member of the Scientific Committee of the *Centre National de la Recherche Scientifique* (C.N.R.S.) (the French National Research Council), a government-funded research organization, under the administrative authority of France's Ministry of Research (http://www.cnrs.fr/comitenational/cs/membres.htm)
2014-today	Appointed *External Examiner* for the BSc Mobile Communications and Security and the BSc Electronics programs of the Department of Electronic & Computer Engineering of the University of Limerick (Ireland)
2014-2016	Elected Chair of the Steering Committee of the IEEE ETS: European Test Symposium
2015-today	Appointed member of the *EuSEM* RC Interest Group "*Technology and Process of Care in Emergency Care*"
2015, 2005, 2004, 2000, 1999, & 1993	Recipient of a "Meritorious Service Award" from the IEEE Computer Society

2013	Elected to the grade of *Senior Member* of the IEEE, in recognition of professional standing
2012	Honored of the title "*Doctor Honoris Causa*" of the Technical University of Cluj-Napoca (Romania) "pentru contributia remarcabila in domeniul proiectarii si testarii sistemelor digitale si pentru activitatea de exceptie in statornicirea si dezvoltarea relatiior de colaborare dintre universitatile noastre"
2011-today	Research Project evaluator for the French ANR: Agence National de la Recherche
2010-today	Member of the Scientific Committee of the *International Test Spring School* organized yearly by the IEEE ETS Steering Committee
2013-today	Vice-Chair of the IFIP (International Federation for Information Processing) Technical Committee TC 10 - Computer Systems Technology aimed at "promoting the State-of-the-Art and the coordination of information exchange on concepts, methodologies, and tools in the stages in the life cycle of computer systems"
2004-2012	Italian Representative within the IFIP Technical Committee TC 10
2007 & 2006	Recipient of an "Outstanding Service Award" from the IEEE Computer Society
2006	Nominated and selected by the Senate of the University of Cyprus to serve on the Selection Committee for two new faculty positions in the area of Computer Engineering for the newly developed Department of Electrical and Computer Engineering at the University of Cyprus
2000-2003	Elected chair of the IEEE - Computer Society TTTC: Test Technology Technical Council. The TTTC is a worldwide professional organization sponsored by the IEEE Computer Society. TTTC's goals are to contribute to members' professional development and advancement, to help them solve engineering problems in electronic test, and to help advance the state-of-the art
1998-1999	Elected vice-chair of the IEEE - Computer Society TTTC: Test Technology Technical Council
1994-1997	Elected chair of the European Group (ETTTC) of the IEEE - Computer Society TTTC: Test Technology Technical Council
1993-1997	Appointed by C.N.R.S. (the French National Research Council), member of the Scientific Committee of LIRMM, (Laboratoire d'informatique, de robotique et de Microelectronique) in Montpellier (France)
1997	Recipient of the CV Ramamoorthy best paper award at ICTAI'97: the 9th IEEE International Conference on Tools with Artificial Intelligence, Newport Beach, CA (USA)
1996	Appointed member of the Evaluation Committee of the GMD System Design Technology Institute in Bonn (Germany)
1996	Honored as Golden Core Member of IEEE Computer Society "as one of the distinguished core of dedicated volunteers and staff whose leadership and service have made the IEEE Computer Society the world's preeminent association of computing professionals.
1989-today	Appointed member of several Evaluation Committees (Jury) abroad; the complete list is in Appendix B.

Domestic

2013-today	Elected President of CINI (Consorzio Interuniversitario Nazionale per l'Informatica): a consortium of 43 Italian universities that do research in Computer Engineering & Computer Science, deliver MS/PhD degrees, and are public funded) http://www.consorzio-cini.it
2006-2013	Vice-President of CINI
2001-today	Member of the Executive Committee of CINI
1994-today	Member of the Board of Governors of CINI
2008-today	Director of the Schools on "Complex Software System Design" organized by CINI for the Selex S.I. Company
2006-2010	Member of the Scientific & Technical Committee of CSI Piemonte, one of the largest Italian ICT operators in the public sector
2007-2008	Appointed by the Minister Prof. Luigi Nicolais, member of the "Commissione interministeriale permanente per l'impiego delle tecnologie ICT per le categorie deboli e svantaggiate"
2002-2008	Member of the Scientific Committee of the Italian Annual National Summer School for Ph.D. students in Computer Engineering
2004-2006 & 2014-today	Appointed Member of the Board of Governors of the Associazione Italiana per l'Informatica e il Calcolo Automatico (AICA) (First and more important Italian association of ICT professionals)
2001-2005	Elected chair of the Gruppo di Ingegneria Informatica (G.I.I.), which collects all the professors and researches teaching in Italian Computer Engineering Faculties
1998-2002	Appointed by the Italian Minister for Industries, member of the Board of Governors of the Istituto Elettrotecnico Nazionale Galileo Ferraris in Torino (Italy)
1999 & 2000	Director of the Italian Annual National Summer School for Ph.D. students in Computer Engineering
1989 & 1992	Director of the Advanced Courses "Formal Verification Techniques in VLSI Design", at Scuola Superiore G. Reiss Romoli dell'Aquila, AQ (Italy)
1988	Director of the 3rd Advanced Course on "VLSI Testing", at Csata-Tecnopolis, Valenzano, BA (Italy)

Academic

2006-2007	Chair of the Engineering Track and of the Long-Life-Learning program of the Università Internazionale Telematica UniNettuno, Rome, Italy
1994-1999	Dean of the "Consiglio del Settore dell'Informazione" (Corsi di Laurea in Ing. Elettronica, Informatica e delle Telecomunicazioni) of the Politecnico di Torino, Turin (Italy)
1992-1993	Vice-Director of the Dipartimento di Automatica e Informatica (CE Dept) of the Politecnico di Torino, Turin (Italy).

Publications

A list of the most significant international publications can be found at:
- On the system www.scholar.google.com:
  http://scholar.google.com/citations?user=KK2B5igAAAAJ
- On the system www.scopus.com:
  http://www.scopus.com/authid/detail.url?authorId=7005222576

Author and/or co-author of several scientific publications, including:
- 70+ published on journals and magazines
- 220+ presented at international congresses
- 3 scientific books
- 7 teaching books

Tutoring
- Advisor of 100+ Master Thesis
- Advisor of 20+ Ph.D. Students.

Humanitarian Activities
- Deeply involved in several solidarity and international cooperation activities of the Voluntary Association *Comitato Girotondo*, towards children and people contaminated by the Chernobyl nuclear disaster (http://www.comitato-girotondo.org)
- Project leader of the projects:
  – "ECDL in Belarus"
  – "EC4BC European Certifications for Belarusian Children"
  – "CUROMA".

Appendix A : Program Committee member

- AICA Congresso annuale AICA
  - 1992, Torino (Italy)

- AQTR International Conference on Automation, Quality & Testing, Robotics
  - 2002, Cluj-Napoca (Romania)
  - 2004, Cluj-Napoca (Romania)
  - 2006, Cluj-Napoca (Romania)
  - 2008, Cluj-Napoca (Romania)
  - 2010, Cluj-Napoca (Romania)
  - 2012, Cluj-Napoca (Romania)
  - 2014, Cluj-Napoca (Romania)

- ASAP International Conference on Application-specific Systems, Architectures and Processors
  - 1993, Venezia (Italy)
  - 1996, Chicago IL (USA)

- ATS IEEE Asian Test Symposium
  - 1996, Hsinchu (Taiwan)
  - 2001, Kyoto (Japan)
  - 2002, Guam (USA)
  - 2003, Xi'an (P.R. China)
  - 2004, Kenting (Taiwan)
  - 2006, Fukuoka (Japan)
  - 2008, Sapporo (Japan)

- BEC Baltic Electronics Conference
  - 1996, Tallinn (Estonia)
  - 1998, Tallinn (Estonia)
  - 2000, Tallinn (Estonia)
  - 2002, Tallinn (Estonia)
  - 2004, Tallinn (Estonia)
  - 2006, Tallinn (Estonia)

- CAA Conference AICA-ANIPLA
  - 1988, Informatica e Automazione di fabbrica, Torino (Italy)

- CHARME IFIP WG 10.5 Advanced Research Working Conference on "Correct Hardware Design and Verification Methods"
  - 1989, Leuven (Belgium)
  - 1991, Torino (Italy) (General Chair)
  - 1993, Arles (France)
  - 1995, Frankfurt (Germany)
  - 1997, Montréal, Québec (Canada)

- CHDL International Symposium on Computer Hardware Description Languages and their Applications
  - 1987, Amsterdam (The Netherlands)
  - 1989, Washington DC (USA)
  - 1991, Marseilles (France)
  - 1993, Ottawa (Canada)
  - 1997, Toledo (Spain)

- CompEuro'87: VLSI and Computers
  - 1987, Hamburg (Germany)

- DASIP Conference on Design and Architectures for Signal and Image Processing
  - 2013, Cagliari (Italy) (Track chair of Advanced Image Processing for Space Applications)

- DATE Design Automation and Test in Europe
  - 1999, Munich (Germany)
  - 2000, Paris (France)
  - 2001, Munich (Germany) (Design for Testability - Topic Chair)
  - 2002, Paris (France) (BIST & Design for Testability - Topic Chair)
  - 2003, Munich (Germany) (BIST & Design for Testability - Topic Co-Chair)
  - 2007, Nice (France)
  - 2008, Munich (Germany) (Design for Test and BIST - Topic Co-Chair)
  - 2009, Nice (France)
  - 2010, Dresden (Germany)
  - 2011, Grenoble (France)
  - 2012, Dresden (Germany)
  - 2013, Grenoble (France)
  - 2014, Dresden (Germany) (Test Access, Design-for-Test, Test Compression, System Test - Topic Co-Chair)
  - 2015, Grenoble (France) (Design-for-Test, Test Compression and Access - Topic Chair)
  - 2016, Dresden (Germany)

- DATICS IEEE Ninth International Symposium on Parallel and Distributed Processing with Applications Workshops
  - 2011, Busan (Korea)

- DCIS Conference on Design of Circuits and Integrated Systems
  - 2014, Madrid (Spain)

- DDECS Design & Diagnostics of Electronic Circuits & Systems
  - 1998, Szczyrk (Poland)
  - 2000, Smolenice (Slovakia)
  - 2001, Gyor (Hungary)
  - 2002, Brno (Czech Republic)
  - 2003, Poznan (Poland)
  - 2004, Tatranska Lomnica (Slovakia)
  - 2006, Prague (Czech Republic)
  - 2007, Kraków (Poland)
  - 2008, Bratislava (Slovakia)
  - 2009, Liberec (Czech Republic)
  - 2010, Vienna (Austria)
  - 2011, Cottbus (Germany)
  - 2012, Tallinn (Estonia)
  - 2013, Karlovy Vary (Czech Republic)
  - 2014, Warsaw (Poland) (Track Chair)
  - 2015, Belgrade (Serbia)
  - 2016, Kosice (Slovakia)

- DELTA IEEE International Symposium on Electronic Design, Test and Applications
  - 2002, Christchurch (New Zealand)
  - 2004, Perth (Western Australia)
  - 2006, Kuala Lumpur (Malaysia)

- 2008, Hong Kong (China)
- 2010, Ho Chi Minh City (Vietnam)
- 2011, Queenstown (New Zealand)

- **DFM&Y IEEE International Workshop on Design for Manufacturability & Yield**
  - 2006, Santa Clara, CA (USA)

- **DTIS IEEE International Conference on Design & Technology of Integrated Systems**
  - 2006, Tunis (Tunisia)
  - 2007, Rabat (Morocco)
  - 2008, Tozeur (Tunisia)
  - 2009, Cairo (Egypt)
  - 2010, Hammamet (Tunisia)
  - 2011, Athens (Greece)
  - 2013, Abu Dhabi (UAE)
  - 2014, Santorini (Greece) (Program Chair)
  - 2015, Naples (Italy) (General Chair)

- **EDAC The European Design Automation Conference**
  - 1990, Glasgow (Scotland)
  - 1992, Brussels (Belgium)
  - 1993, Paris (France) (Poster sections chair)

- **EDfTW IEEE European Workshop on Design For Testability**
  - 1987, Castiglion della Pescaia GR (Italy) (General Chair)

- **ED&TC The European Design and Test Conference**
  - 1994, Paris (France) ("ATPG" Topic Chair)
  - 1995, Paris (France) ("Test program development tools and techniques" Topic Chair)
  - 1996, Paris (France)
  - 1997, Paris (France)

- **ETC European Test Conference**
  - 1989, Paris (France)
  - 1991, Munich (Germany)
  - 1993, Paris (France) (Tutorial Chair)

- **ETS European Test Symposium**
  - 2004, Ajaccio (France) (Program Chair)
  - 2005, Tallinn (Estonia) (General Co-Chair)
  - 2006, Southampton (UK)
  - 2007, Freiburg (Germany)
  - 2008, Verbania (Italy)
  - 2009, Sevilla, (Spain)
  - 2010, Prague (Czech Republic) (Embedded Tutoriial Chair)
  - 2011, Trondheim (Norway)
  - 2012, Annecy (France)
  - 2013, Avignon (France)
  - 2014, Paderborn (Germany)
  - 2015, Cluj Napoca (Romania) (Program Chair)
  - 2016, Amsterdam (The Netherlands)

- **ETW IEEE European Test Workshop**
  - 1996, Sète (France) (Vice General Chair)

- 1997, Baia Chia CA (Italy) (General Chair)
  - 1998, Sitgers (Spain) (Topic Chair)
  - 1999, Constance (Germany)
  - 2000, Cascais (Portugal) (Program chair)
  - 2001, Stockholm (Sweden) (Topic Chair)
  - 2002, Corfu (Greece) (Topic Chair)
  - 2003, Maastricht (The Netherlands)

- EURO-DAC: European Design Automation Conference with EURO-VHDL and Exhibition
  - 1996, Genève (Switzerland)
  - 1997, Düsseldorf (Germany)

- EUROMICRO Symposium on Microprocessing and Micro-programming
  - 1987, Portsmouth (UK)
  - 1988, Zurich (Switzerland)
  - 1989, Cologne (Germany)
  - 1991, Vienna (Austria)
  - 1992, Paris (France)

- EWDC: European Workshop on Dependable Computing
  - 1994, Carcassonne (France) (Program Co-Chair)

- EWDTS: IEEE East West Design and Test Symposium
  - 2007, Yerevan (Armenia)
  - 2008, Lviv (Ukraine)
  - 2009, Moscow (Russia)
  - 2010, St. Petersburg (Russia) (General ViceChair)
  - 2011, Sevastopol (Ukraine) (General ViceChair)
  - 2012, Kharkov (Ukraine) (General ViceChair)
  - 2013, Rostov-on-Don (Russia) (General ViceChair)
  - 2014, Kiev (Ukraine) (General ViceChair)
  - 2015, Batuni (Georgia) (General ViceChair)

- EWDTW: IEEE East West Design and Test Workshop
  - 2004, Yalta Crimea (Ukraine) (Program ViceChair)
  - 2005, Odessa (Ukraine) (Program ViceChair)
  - 2006, Sochi (Russia)

- Formal Methods in VLSI Design ACM SIGDA International Workshop
  - 1991, Miami, FL (USA)

- HLDVT: IEEE International High Level Design Validation and Test Workshop
  - 1996, Oakland CA (USA)
  - 2002, Cannes (France)
  - 2003, San Francisco, CA (USA) (European Liason)

- ICCAD: IEEE International Conference on Computer-Aided Design
  - 1991, Santa Clara CA (USA)

- ICCD: IEEE International Conference on Computer Design
  - 1988, Rye Brook NY (USA)
  - 1989, Rye Brook, NY (USA) (European representative)
  - 1990, Cambridge MA (USA) (European representative)
  - 1991, Cambridge MA (USA)

- o 1992, Cambridge MA (USA)
- o 1993, Cambridge MA (USA)
- o 1994, Boston MA (USA)
- o 1995, Austin TX (USA)

- • IDT: IEEE International Design & Test Symposium
    - o 2012, Doha (Qatar)
    - o 2013, Marrakech (Morocco) (Program Chair)
    - o 2014, Algiers (Algeria) (Special Sessions Co-Chair)
    - o 2015, Dead Sea (Jordan) (Special Sessions Co-Chair)

- • IMSTW: IEEE International Mixed Signal Testing Workshop
    - o 1995, Villard de Lans (France) (ETTTC Liason)

- • I-IP: IEEE International Workshop on Infrastructure IP
    - o 2003, Charlotte NC (USA) (Publication Chair)
    - o 2004, Charlotte NC (USA) (Publication Chair)
    - o 2006, Berkeley CA (USA) (Publication Chair)

- • IOLTS: International On-Line Testing Symposium
    - o 2003, Kos Island (Greece)
    - o 2004, Madeira Island (Portugal)
    - o 2005, Saint Raphael (France)
    - o 2006, Como (Italy)
    - o 2007, Crete (Greece)
    - o 2008, Rhodes (Greece)
    - o 2009, Sesimbra-Lisbon (Portugal)
    - o 2010, Corfu (Greece)
    - o 2011, Athens (Greece)
    - o 2012, Sitges (Spain)
    - o 2013, Crete (Greece)
    - o 2014, Platja d'Aro, Catalunya (Spain)
    - o 2015, Halkidiki (Greece)

- • IOLTW: International On-Line Testing Workshop
    - o 1995, Nice (France)
    - o 1996, Biarritz (France)
    - o 1997, Crete (Greece)
    - o 1998, Capri NA (Italy)

- • ITC: IEEE International Test Conference
    - o 1992, Baltimore MD (USA) (European sub-committee chair)
    - o 1993, Baltimore MD (USA) (European sub-committee chair)
    - o 1994, Washington DC (USA) (European sub-committee chair)
    - o 1995, Washington DC (USA) (European sub-committee chair)
    - o 1996, Washington DC (USA) (European sub-committee chair)
    - o 1997, Washington DC (USA) (European sub-committee chair)
    - o 1998, Washington DC (USA) (European sub-committee chair)
    - o 1999, Atlantic City (USA) (European sub-committee vice-chair)
    - o 2000, Atlantic City (USA) (European sub-committee vice-chair)
    - o 2001, Baltimore MD (USA) (FPGA Topic Chair)
    - o 2002, Baltimore MD (USA) (FPGA Topic Chair)
    - o 2003, Charlotte, NC (USA)
    - o 2004, Charlotte, NC (USA)

- o 2005, Austin, TX (USA)
- o 2006, Santa Clara, CA (USA)
- o 2007, Santa Clara, CA (USA) (Memory Topic Chair)

- MTDT: IEEE International Workshop on Memory Technology, Design, and Testing
  - o 2006, Taipei (Taiwan)

- MTV: IEEE International Workshop Microprocessor Test and Verification
  - o 2004, Austin, TX (USA)
  - o 2005, Austin, TX (USA)
  - o 2006, Austin, TX (USA)
  - o 2010, Austin, TX (USA)
  - o 2011, Austin, TX (USA)
  - o 2012, Austin, TX (USA)
  - o 2013, Austin, TX (USA)
  - o 2014, Austin, TX (USA)
  - o 2015, Austin, TX (USA)

- S4D: System, Software, SoC and Silicon Debug Conference
  - o 2009, Sophia Antipolis (France)
  - o 2010, Southampton (UK)
  - o 2011, Grenoble (France)
  - o 2012, Vienna (Austria)

- SBCCI: Symposium on Integrated Circuits and System Design
  - o 2010, São Paulo (Brazil)
  - o 2011, João Pessoa (Brazil)
  - o 2012, Brasilia (Brazil)
  - o 2013, Curitiba (Brazil)

- SDD: IEEE International Workshop on Silicon Debug and Diagnosis
  - o 2006, Santa Clara, CA (USA)
  - o 2008, San Diego, CA (USA)
  - o 2010, Dresden (Germany)

- SEDA: The 5th international Conference in Software Engineering for Defense Applications
  - o 2016, Rome (Italy)
-

  SOQUA: International Workshop on SOFTWARE QUALITY ASSURANCE
  - o 2007, Dubrovnik (Croatia)

- TACoS: Test and Analysis of Component Based Systems
  - o 2003, Warsaw (Poland)

- TPCD: IFIP Conference on Theorem Powers in Circuit Design Theory, Practice and Experience
  - o 1992, Nijmegen (The Netherlands)

- TECS: IEEE International Workshop on Testing Embedded Core-based System Chips
  - o 2001, Marina Del Rey CA (USA)

- TRP: IEEE International Workshop on Test Resource Partitioning
  - o 2001, Baltimore MD (USA) (Publicity Chair)
  - o 2002, Baltimore MD (USA) (Publicity Chair)
  - o 2003, Napa Valley CA (USA) (Publicity Chair)

- o 2004, Napa Valley CA (USA) (Publicity Chair)

- VALID: International Conference on Advances in System Testing and Validation Lifecycle
  - o 2009, Porto (Portugal)
  - o 2010, Nice (France)
  - o 2011, Barcelona (Spain)
  - o 2012, Lisbon (Portugal)
  - o 2013, Venice (Italy)
  - o 2014, Nice (France)
  - o 2015, Barcelona (Spain)

- VHDL Forum for CAD in Europe
  - o 1993, Innsbruck (Austria)
  - o 1993, Hamburg (Germany)
  - o 1994, Grenoble (France)
  - o 1995, Nantes (France)
  - o 1996, Dresden (Germany)

- VHDL Methods
  - o 1990, Marseilles (France)

- VHDL User's Forum in Europe SIG-VHDL
  - o Spring'97 Working Conference 1997, Toledo (Spain)

- VLSI-SoC: IFIP/IEEE International Conference on Very Large Scale Integration
  - o 2013, Istanbul (Turkey)

- VTS: IEEE VLSI Test Symposium
  - o 1996, Princeton NY (USA)
  - o 1997, Monterey CA (USA)
  - o 1998, Monterey CA (USA)
  - o 1999, Dana Point CA (USA)
  - o 2000, Montreal (Canada)
  - o 2001, Marina Del Rey CA (USA) (Publicity Chair)
  - o 2002, Monterey CA (USA) (Publicity Chair)
  - o 2003, Napa Valley CA (USA) (Vice-Program Chair)
  - o 2004, Napa Valley CA (USA)
  - o 2005, Palm Springs, CA (USA) (Vice-Program Chair)
  - o 2006, Berkeley, CA (USA) (Program Chair)
  - o 2007, Berkeley, CA (USA) (General Chair)
  - o 2008, San Diego, CA (USA)
  - o 2009, Santa Cruz, CA (USA)
  - o 2010, Santa Cruz, CA (USA)
  - o 2011, Dana Point CA (USA)
  - o 2012, Maui, Hawaii (USA)
  - o 2013, Berkeley, CA (USA)
  - o 2014, Napa, CA (USA)
  - o 2015, Napa, CA (USA)
  - o 2016, Las Vegas, NE (USA)

- WTW: IEEE International Workshop on Test of Wireless Circuits and Systems
  - o 2001, Baltimore MD (USA) (Publicity Chair)

Appendix B : Member of Evaluation Committees Abroad

Estonia

- PhD Thesis Defense:

  o Jaan RAIK   - 30 11 2001 – Un. Tallinn

Finland

- PhD Thesis Defense (Opponent):

  o Masoumeh Azin EBRAHIMI   - 28 06 2013 – Un. Turku

France

- Thèses pour obtenir le titre de Docteur (Rapporteur):

  o Francois DARLAY   - 20 11 1989 - INPG Grenoble
  o Frédéric BANCEL   - 01 07 1993 - Un. Montpellier II
  o Mokhtar BOUDJIT   - 19 05 1995 - INPG Grenoble
  o Michel ALLEMAND   - 15 06 1995 - Un. de Provence (Aix-Marseille I)
  o Claude BARON   - 02 11 1995 - I.N.S.A. de Toulouse
  o Catherine BAYOL   - 13 12 1995 - Un. Joseph Fourier Grenoble
  o Francoise MARTINOLLE   - 03 05 1995 - I.N.S.A. de Toulouse
  o Alain DARGELAS   - 24 03 1997 - Un. Montpellier II
  o Ricardo PIRES   - 15 07 1998 - Un. Montpellier II
  o Ghassan AL-HAYEK   - 10 04 1999 - INPG Grenoble
  o Aurélia PETITQUEUX   - 24 10 2000 - Un. Montpellier II
  o Mounir BENABDENBI   - 27 09 2002 - Un. Pierre et Marie Curie – Paris VI
  o Philippe FAURE   - 19 12 2002 - Un. Montpellier II
  o Luigi DILILLO   - 08 06 2005 - Un. Montpellier II
  o Renan ALVES FONSECA   - 21 07 2011 - Un. Montpellier II
  o Panagiota PAPAVRAMIDOU   - 19 11 2014 - Un. de Grenoble
  o Fatemeh JAVAHERI   - 01 10 2015 - Un. Grenoble Alpes
  o Saif UR REHMAN   - 06 11 2015 - Un. de Grenoble

- Habilitation à diriger des Recherches (Rapporteur):

  o Bruno ROUZEYRE   - 13 02 1992 - Un. Montpellier II
  o Meriyem MARZOUKI   - 20 02 1998 - INPG Grenoble
  o Hassen AZIZA   - 10.11.2012 - Polytech Marseille
  o Arnaud VIRAZEL   - 21 11 2014 - Un. Montpellier II
  o Luigi DILILLO   - 25 09 2015 - Un. Montpellier

Germany

- PhD Thesis Defense:

  o Stefan HOLST   - 28 09 2012 - Un. of Stuttgart
  o Nadereh HATAMI MAZINANI   - 14 03 2014 - Un. of Stuttgart

The Nederlands

- PhD Thesis Defense:

    o   Sandra IROBI   - 15 09 2011 - TUDelft